M rows of opposite electrodes, each said row of opposite electrodes arranged opposite one row of pixel electrodes through a liquid crystal layer;

a scanning line driving circuit operable to supply a scanning signal including a scanning period for selecting at least one of the M rows of scanning lines to the M rows of scanning lines; and

a polarity inverting circuit operable to invert a polarity of a voltage applied to the liquid crystal layer by changing a voltage supplied to an opposite electrode of a row corresponding to the selected scanning line in synchronization with the scanning period.

Please replace the text of claim 17 with the following text:

17. A substrate opposite to an active matrix substrate with a liquid crystal layer therebetween, wherein the substrate comprises:

M rows of scanning lines, wherein M is an integer equal to or greater than 2, and N columns of data lines, wherein N is an integer equal to or greater than 2;

M × N number of switching elements, each of said switching elements connected to one of the M rows of scanning lines and one of the N columns of data lines; and

M × N number of pixel electrodes, each of said pixel electrodes connected to one of the M × N number of switching elements,

wherein the substrate includes M rows of opposite electrodes, each of said row of opposite electrodes arranged oppositely to a row of the M × N number of pixel electrodes in a rectangular shape, wherein the M rows of opposite electrodes are insulated from each other.

REMARKS

Claims 9, 10 and 17 are amended; marked up versions of the amended claims are attached hereto pursuant to 37 C.F.R. § 1.121(c)(ii).

In view of the foregoing, it is respectfully submitted that the application is in condition for allowance.

If for any reason the Examiner finds the application other than in condition for allowance, the Examiner is requested to call the undersigned attorney at the Los Angeles, California telephone number (213) 337-6700 to discuss the steps necessary for placing the application in condition for allowance.

If there are any fees due in connection with the filing of this response, please charge the fees to our Deposit Account No. 50-1314.

Respectfully submitted,

HOGAN & HARTSON L.L.P.

Date: July 12, 2001

By: _____

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Version with markings to show changes made:

9. Electronic equipment comprising a liquid crystal device according to [any one of claims 1 to] claim 8.

10. A driving device for a liquid crystal display panel comprising:

M rows of scanning lines, wherein M is an integer equal to or greater than 2, and N columns of data lines, wherein N is an integer equal to or greater than 2;

M × N number of switching [element] elements, each said switching element [respectively] connected to one of the M rows of scanning lines and one of the N columns of data lines;

M × N number of pixel electrodes, each said pixel electrode [respectively] connected to one of the [M × N number of switching element] switching elements;

M rows of opposite electrodes, each said row of opposite electrodes arranged [oppositely to respective rows of the M × N number] opposite one row of pixel electrodes through a liquid crystal layer;

a scanning line driving circuit [which supplies] operable to supply a scanning signal including a scanning period for selecting at least one of the M rows of scanning lines to the M rows of scanning lines; and

a polarity inverting circuit [which inverts] operable to invert a polarity of a voltage applied to the liquid crystal layer by changing a voltage supplied to an opposite electrode of a row corresponding to the selected scanning line in synchronization with the scanning period.

17. A substrate opposite to an active matrix substrate with a liquid crystal layer [there between] therebetween, [ ] wherein the substrate comprises:

M rows of scanning lines, wherein M is an integer equal to or greater than 2, and N columns of data lines, wherein N is an integer equal to or greater than 2;

M × N number of switching [element] elements, each of said switching elements [respectively] connected to one of the M rows of scanning lines and one of the N columns of data lines; and

M × N number of pixel electrodes, each of said pixel electrodes [respectively] connected to one of the M × N number of switching [element] elements,

wherein the substrate includes M rows of opposite electrodes, each of said row of opposite electrodes arranged oppositely to [respective rows] a row of the M × N number of pixel electrodes[,] in a rectangular shape, wherein the M rows of opposite electrodes [being] are insulated from each other.